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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : 2616  
Serial No. : 10/024,657 Examiner : Jay P. Patel  
Filed : December 18, 2001 Assignee : Intel Corporation  
Title : CONTROL MECHANISMS FOR ENQUEUE AND DEQUEUE OPERATIONS  
IN A PIPELINED NETWORK PROCESSOR

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REPLY TO ACTION OF APRIL 14, 2006

Applicant thanks the Examiner for participating in telephone conferences on May 3, 2006 and May 5, 2006 with Mr. Joseph Valentino.

The previous rejections of the claims were maintained in the final Office action as follows:

(1) Claims 1-4, 6 and 21-25 were rejected as anticipated by Calvignac et al (U.S. Patent No. 6,658,546 B2).

(2) Claims 5, 7-8, 9-20 and 26-28 were rejected as unpatentable over Calvignac et al. as applied to claims 1 and 21 and further in view of Wilford et al (U.S. Patent No. 6,687,247).

Applicant respectfully requests reconsideration and withdrawal of the claim rejections in view of the following remarks.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Date of Deposit MAY 11, 2006

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